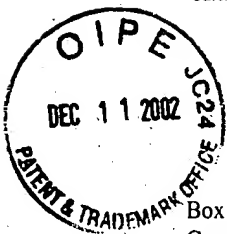


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Docket No.: M-10889 US

December 11, 2002

Box Non-Fee Amendment
Commissioner For Patents
Washington, D.C. 20231

Re: Applicants: Timothy K. Carns, Lee J. DeBruler, John L. Horvath, Michael J. Westphal
Assignee: ZiLOG, Inc.
Title: PROCESS TO IMPROVE HIGH PERFORMANCE CAPACITOR PROPERTIES IN INTEGRATED MOS TECHNOLOGIES
Application No.: 09/351,544 Filing Date: July 12, 1999
Examiner: Brock II, Paul E. Group Art Unit: 2815
Docket No.: M-10889 US Conf. No.: 9910

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard;
- (2) This Transmittal Letter (in duplicate); and
- (3) Amendment (23 pages).

- ☒ No additional fee is required.
☐ The fee has been calculated as shown below:

CLAIMS AS AMENDED

	Claims Remaining <u>After Amendment</u>		Highest No. Previously <u>Paid For</u>		Present <u>Extra</u>		<u>Rate</u>	Additional <u>Fee</u>
Total Claims	43	Minus	61	=	0	x	\$18.00	\$
Independent Claims	6	Minus	12	=	0	x	\$84.00	\$
<input type="checkbox"/>	Fee of _____ for the first filing of one or more multiple dependent claims per application							\$
<input type="checkbox"/>	Fee for Petition for Extension of Time							\$
<input type="checkbox"/>	Fee for							\$

Total additional fee for this Amendment:

- ☒ Conditional Petition for Extension of Time: If an extension of time is required for timely filing of the enclosed document(s) after all papers filed with this transmittal have been considered, an extension of time is hereby requested.
- ☒ Please charge our Deposit Account No. 19-2386 in the amount of \$0.00
- ☒ Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 19-2386.

Total: \$0.00

EXPRESS MAIL LABEL NO.:

EV259159804US

Respectfully submitted,

Michael G. Cleveland
Reg. No. 46,030



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

23/D

12/23/02

Smith

Applicants: Timothy K. Carns, Lee J. DeBruler, John L. Horvath, Michael J. Westphal
Assignee: ZiLOG, Inc.
Title: PROCESS TO IMPROVE HIGH PERFORMANCE CAPACITOR PROPERTIES IN INTEGRATED MOS TECHNOLOGIES
Application No.: 09/351,544 Filing Date: July 12, 1999
Examiner: Brock II, P. Group Art Unit: 2815
Docket No.: M-10889 US Conf. No.: 9910
(Formerly ZILG.204US0)

San Francisco, California
December 11, 2002

Box Non-Fee Amendment
Commissioner for Patents
Washington, D. C. 20231

AMENDMENT

Dear Sir:

Responsive to the Official Action mailed on September 11, 2002, applicant responds to the Official Action as follows:

In the Claims:

Please cancel, without prejudice, claims 40-71.

Please replace claim 3 with the following (a marked up version is in the Appendix):

--3.(Thrice Amended) A method of forming a capacitor in an integrated circuit comprising:
forming a bottom electrode layer on a semiconductor body;
forming a dielectric layer over at least a portion said bottom electrode;
forming a top electrode layer over at least a portion of said dielectric layer;

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Application No.: 09/351,544
904293 v1

Express Mail No.: EV259159804US